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022434 7590 12/17/2003

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AGNES SPENCE (Depositor's name)
 Agnes Spence (Signature)
 MARCH 16, 2004 (Date)

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 09/747,703 | 12/21/2000 | Thomas D. Nguyen | LAM1P155/P0700 | 9960 |

TITLE OF INVENTION: METHOD AND APPARATUS FOR REDUCING THE BACKSIDE FAULTS DURING WAFER PROCESSING

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1330 | \$300 | \$1630 | 03/17/2004 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|-------------------|----------|----------------|
| KORNAKOV, MICHAEL | 1746 | 134-006000 |

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

LAM RESEARCH CORPORATION

FREMONT, CA

Please check the appropriate assignee category or categories (will not be printed on the patent);

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(Authorized Signature) Quinn C. Howell (Date) 3-16-04
 QUINN C. HOWELL WARTH, REG. NO. 45,738

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